

Review

Pursuing High Quality Phase-Only Liquid Crystal on Silicon (LCoS) Devices

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Abstract: Fine pixel size and high-resolution liquid crystal on silicon (LCoS) backplanes have been developed by various companies and research groups since 1973. The development of LCoS is not only beneficial for full high definition displays but also to spatial light modulation. The high-quality and well-calibrated panels can project computer generated hologram (CGH) designs faithfully for phase-only holography, which can be widely utilized in 2D/3D holographic video projectors and components for optical telecommunications. As a result, we start by summarizing the current status of high-resolution panels, followed by addressing issues related to the driving frequency (i.e., liquid crystal response time and hardware interface). LCoS panel qualities were evaluated based on the following four characteristics: phase linearity control, phase precision, phase stability, and phase accuracy.

Keywords: liquid-crystal-on-silicon; spatial light modulator; holographic display; phase precision and stability; phase accuracy; spatially anamorphic phenomenon

1. Introduction

Photo-activated liquid crystal on zinc-sulfide (ZnS) [1], cadmium-sulfide (CdS) [2], and active-matrix addressed liquid-crystal-on-silicon (LCoS) [3] were first introduced by Hughes Research Laboratories for display applications in the early 1970s. Later, this approach was extended to the reflective spatial light modulator (SLM) [4,5]. The LCoS-based SLM panel resolution increased from 16×16 to 176×176 pixels, as was reported by the University of Edinburgh (Edinburgh, UK), in the period 1986–1989 [6]. In 1993, resolution was further increased to a 254×254 pixel array by DisplayTech (now CITIZEN FINEDEVICE (CFD) Co., LTD, Yamanashi, Japan). Their panel adopted the fast-switching ferroelectric liquid crystal (FLC) and implemented the field-sequential color system to generate a full color display. The ferroelectric-LCoS remains their distinctive product until today [7]. The projection display technology using LCoS as the core optical engine is not able to compete with digital micro-mirror device (DMD) technology with a resolution of 720p due to the cost structure. However, a high-resolution LCoS panel with a small pixel size can be commercialized owing to the advancement in integrated circuit (IC) technology during the past 10 years. Since 2000, LCoS has evolved from 720p to not only 2K1K Full High Definition (FHD) but also 4K2K resolution. Photos shown in Figure 1 of high-resolution display products are provided by Jasper Display Corp.

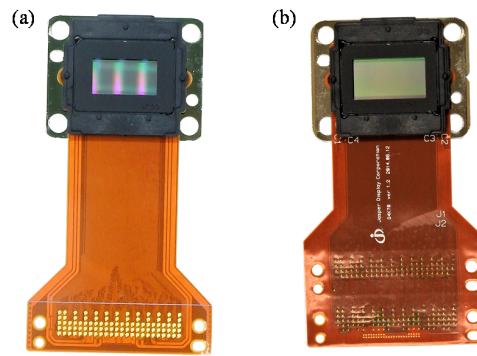


Figure 1. Photos of (a) 2K1K and (b) 4K2K panels from Jasper Display Corp.

Holographic near-eye displays using LCoS as a display panel opened a new era for virtual and augmented realities. Oculus Rift and HTC VIVE announced and demonstrated compelling 3D virtual reality (VR) experiences using JD5552 LCoS-SLM (Jasper Display Corp.; Hsinchu, Taiwan R.O.C.) in 2016 [8]. Microsoft demonstrated the HoloLens, its first self-contained augmented reality (AR) device, using PLUTO LCoS-SLM (Holoeye Photonics; AG, Berlin, Germany) in the same year [9]. In addition to Jasper Display Corp. and Holoeye Photonics, there are other worldwide providers of LCoS micro-displays, such as Hamamatsu Photonics, Meadowlark Optics, Santec Corp., and Himax. Each company has its own focus within its products. Jasper Display Corp. has its own digital drive 2K1K and 4K2K panels with corresponding drivers. Holoeye Photonics has a distinctive solution for 60 Hz 4K-SLM and computer-generated hologram (CGH) information input software development kit (SDK) interface design [10]. Hamamatsu provides pure-phase SLM solutions with high linearity and precision. It also provides SLMs for high-power laser applications [11]. Meadowlark Optics features the analog drive LCoS-SLM solution, which has a fast liquid crystal (LC) response time, a near-millisecond response, and a near-kHz picture frame rate [12]. Santec’s LCoS-SLM phase modulator can provide ultra-high flatness (i.e., high phase precision) and a 10-bit linear curve [13]. Table 1 summarizes their unique products.

Table 1. Four main LCoS phase modulator providers and their unique products.

HOLOEYE Photonics AG	Hamamatsu Photonic	Meadowlark Optics	Santec Corp.
Products	Products	Products	Products
1. PLUTO-2: VIS-014, VIS-016, VIS-020, VIS-056 2. LETO: LETO-only 3. GAEA-2: VIS-036	1. X-10468: -01, -02, -03 2. X-13138:-01, -02, -03	1. P512: P-, PDM-, HSP-, HSPDM-, ODP-, ODPDM 2. P1920: P-, HSP-	1. SLM-100: -01 2. SLM-200: -01
Features	Features	Features	Features
1. UHD 4K-Panel (7000 ppi) 2. Driver Interface Up to 600 MHz(HDMI 2.0) 3. SDK Solution	1. Pure, Linear and Precise Phase Control 2. High-Power-Use	1. Analogy Driving Scheme 2. Fast LC Response Time 3. Sub-KHz PCIe Interface	1. Super Flatness Available: $\lambda/40$ 2. 10-bit LUT
* Only List for VIS Light-Region-Use LCoS-SLM ($T_{op} = 35\text{ }^{\circ}\text{C}$)			
* BNS LCoS-SLM is acquired by Meadowlark Optics in 2014			

A phase-only spatial light modulator can be used as a key optical element for displays, adaptive optics for sensing, lithography, and telecommunication, as shown in Figure 2. The linearity of phase modulation, response time, phase precision, and phase stability are key characteristics for appraising or selecting phase-only LCoS-SLM panels for the applications designed. However, it is difficult to obtain a single LCoS possessing all the desired features for all applications. Their specification and performance optimization are application-driven. For example, response time is the current limitation in a holographic display. It may be more important than phase stability, because an excellent holographic image can still be achieved using a well linearly calibrated and high-phase precision LCoS-SLM panel. On the other hand, the phase stability of the LCOS-SLM is a vital property in the field of optical communication, because an instantaneous disturbance resulting from an unstable phase

may cause misjudgment of the transmitted information [14]. Compared to fast response time, phase stability is much more important for optical communication applications.

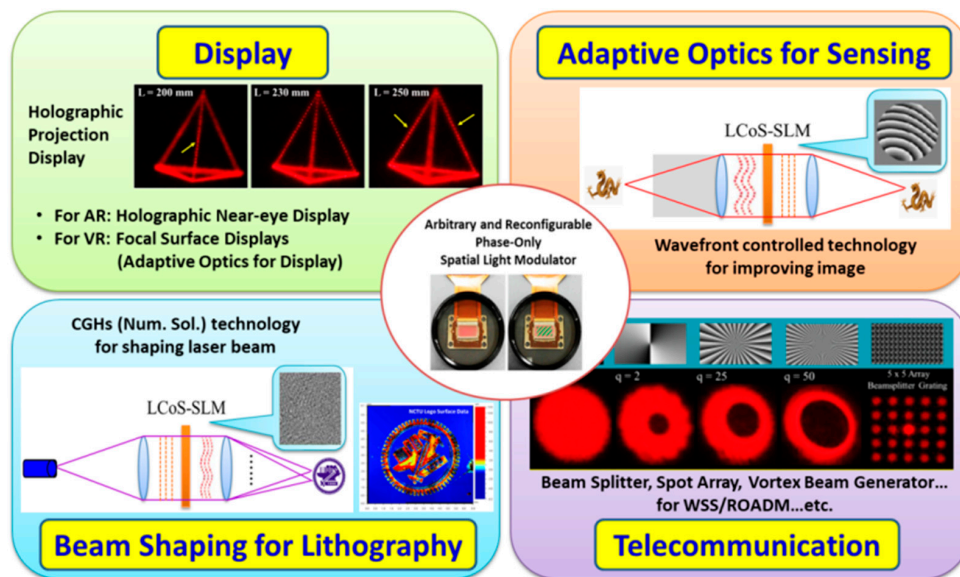


Figure 2. Schematic of various applications of phase-only liquid crystal on silicon spatial light modulator (LCoS-SLM).

Other applications may require precise phase modulation, such as processing of laser materials [15,16] or controlling the wave front for an image enhancement application [17]. We summarize the quantitative assessment indicators for the LCoS-SLM panels in Figure 3. The high-resolution panel is a trend for future phase modulation. The linearity of phase modulation is not only convenient for data acquisition, but also fulfills linear modulation in the diffraction equations derived from the scalar diffraction theory. The phase precision, phase stability, and phase accuracy can be further evaluated based on a linear phase calibrated panel. Real-time modulation depends on the LC response time, driver latency, and the calculation time of CGHs. In this review, we reveal current progress and approaches in designing and developing high-quality phase-only LCoS-SLM with the assessment of following criteria: panel resolution, driving frequency (LC response time and hardware interface), phase linearity control, phase precision, phase stability, and phase accuracy.

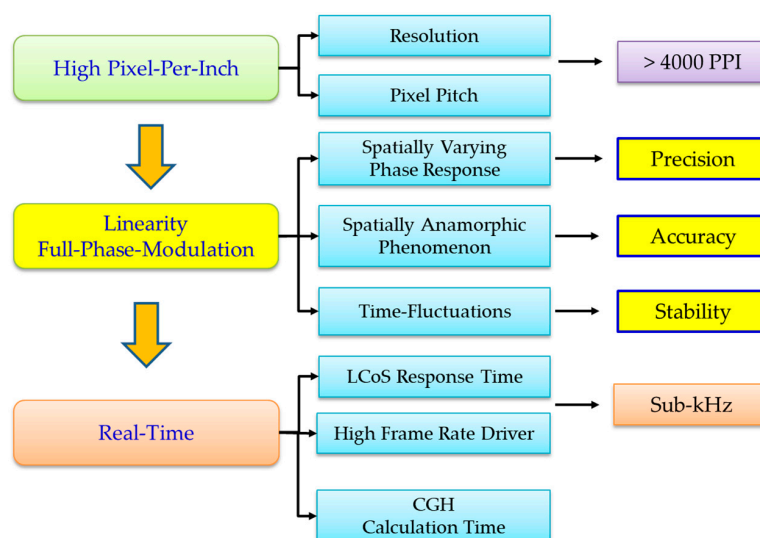


Figure 3. Quantitative assessment indicators for LCoS-SLM.

2. High Resolution and High Pixel-Per-Inch (PPI)

Achieving higher resolution with higher pixel density and smaller pixel pitch is a continuous goal of LCoS micro-displays, especially for near-eye VR display applications [18]. For holographic AR, higher diffraction, or larger image projection, a high-resolution panel is also required [19–21]. The small pixel size enables a large diffraction angle, and a large panel with high resolution empowers finer details of the projected image. The current resolution density in pixel per inch (ppi) and the pixel pitch of commercial LCoS-SLM panels are summarized in Figure 4. In particular, panels with high ppi (>5000) 4K2K or 8K4K LCoS possess a low input frame rate (≤ 60 Hz) or limited driving voltage (< 3.3 V). In general, it is difficult to maintain a frame rate of 60 Hz in a 4K2K panel [22].

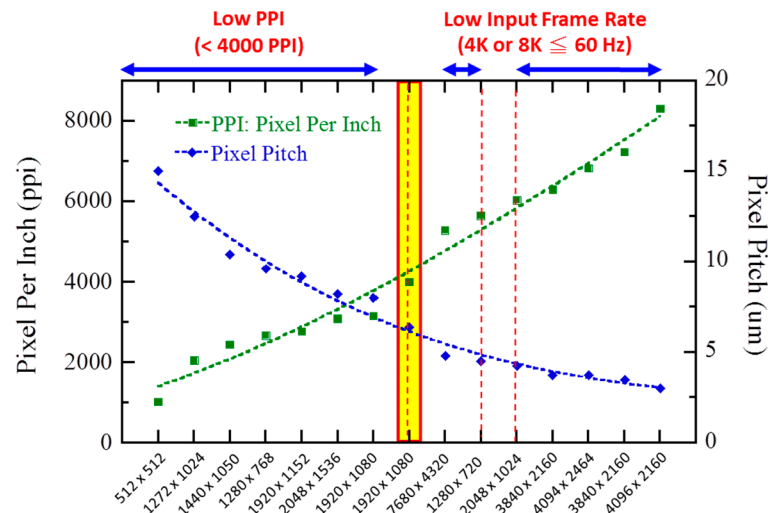


Figure 4. Pixel density and pixel pitch specifications for current LCoS-SLM integrated circuit (IC) backplanes.

Several approaches to achieve high frame rates in high-resolution panels (≥ 4000 ppi) have been realized recently. Yang et al. demonstrated 4000 ppi in a 0.55-inch 1920×1080 IC backplane with a pixel pitch of approximately $6.4 \mu\text{m}$ [23]. Abeeluck et al. demonstrated a digital drive LCoS micro-display with a pixel pitch of $3 \mu\text{m}$ and a high fill factor of 93.5% in resolutions from 1080p to 4K [24]. Such a small panel size can be easily integrated into an optical machine or an embedded system. The advantage of a panel with small pixels is that a larger diffraction angle can be obtained. In addition, two commercial products listed in Figure 4—the 0.26 inch 1280×720 resolution (Holoeye HED2200) and the 0.37 inch 2048×1024 resolution IC back panels (Himax Display; Tainan, Taiwan R.O.C.)—are suitable for panels with a large diffraction angle, while maintaining a high picture frame rate.

3. Switching Time for Phase Modulation

There are four major issues for holographic display applications: (1) the slow liquid crystal response time; (2) the small field of view (FoV); (3) the laser speckle effect degrading the image quality when using lasers as a backlight unit; and (4) a reduction in the frame rate for achieving field-sequential color displays [25]. There are many studies where the aforementioned issues have been addressed, such as: (1) 3–4 ms phase-only panel response time of thin cell gap at $\lambda = 633$ nm to reduce the dynamic reconstruction holographic image blur [26]; (2) the time-division multiplexing method to increase the FoV [27]; and (3) reduction of the speckle effect in image projection using temporal averaging techniques [28]. The ultimate turn-key solution to these challenges is the SLM panel with a sub-millisecond response time and a sub-kHz input frame rate.

Ferroelectric liquid crystal (FLC) modulators [29,30] and digital micro-mirror devices (DMD) [31] are two commercial spatial light modulators capable of achieving sub-millisecond response times. Significant quantization noise lowers the diffraction efficiency due to binary phase modulation. Several

approaches have been proposed to achieve a fast response time, such as dual frequency LC (DFLC) [32], polymer network LC [33,34], and polymer-stabilized blue phase LC [35]. High driving voltage (greater than 10 V) and light scattering in the visible region are the two major drawbacks of these LC material systems. In addition, limited driving voltage in a high-resolution backplane often results in a slower LC response time. Lately, Wu's group reported that an average phase-to-phase response time of ~2 ms can be achieved using high Δn LCs in a thin cell gap of 1.7 μm at an operating voltage of 5 V at 40 $^{\circ}\text{C}$ [36,37]. The reported LCs can be used with 240 Hz frame rate without complicated overdrive or undershoot circuitries. Additionally, the effective birefringence of liquid crystals slightly decreases when the cell gap is below 1.8 μm due to the strong surface boundary effect of the alignment layers [38]. Thus, a higher birefringence LC material may be needed in order to achieve the calculated effective birefringence in an ultra-thin cell for a fast-response LCoS phase modulator. Thalhammer et al. utilized the overdrive approach to boost the LC response time to 1–2 ms for a low-resolution panel (256 \times 256) with 500 Hz input frame rate and ~1.6 kHz data frame rate [39]. However, the input frame rate dropped to 30 Hz when using a High Definition Multimedia Interface (HDMI) controller in their high resolution (1920 \times 1152) LCoS-SLM. Holoeye Photonics presented a HDMI 2.0 Field Programmable Gate Array (FPGA) driver to achieve 1920 \times 1080 resolution (4000 ppi) for 240 Hz and 720 Hz data frame rates for high performance 3D sensing systems [40]. However, the driver scheme cannot support their LETO phase modulator (also 4000 ppi) due to the slow LC response time, which is not comparable with the sub-kHz input frame rates. Meadowlark Optics launched a new 1.8 ms response time phase-only 1920 \times 1152 pixels (~2764 ppi) LCoS-SLM without overdrive. In 2018, the maximum frame rate reached has been 714 Hz by using a PCIe controller [41]. In the same year, Chen's team announced their PCU-3-01 LCoS-SLM, capable of reaching 1.6 ms at 45 $^{\circ}\text{C}$. The panel can be driven at 240 Hz input frame rate with the OCM-ASIC SDK driver (Jasper Display Corp.) and at 720 Hz data frame rate by using an HDMI controller [17]. Higher resolution of 4K2K or 8K4K LCoS panels (greater than 5000 ppi) is limited by low input frame rate (≤ 60 Hz) and low driving voltage (< 3.3 V). Future electronic hardware updates are required to achieve full phase modulation. In summary, less than 2 ms LC response time is available for high-resolution LCoS-SLM panels, but the controller still needs to be updated to reach a frame rate over 500 Hz. The fast LC response of full phase modulation, high input frame rate, and driver interface are equally important to enable next-generation LCoS for holographic displays.

4. Phase Linearity

Phase linearity correction is an essential step for phase modulation panels. This is similar to the gamma correction for linear amplitude modulation in LC displays [42], but is a more complex process, covering the full 2π radians phase. The linear phase response is a key criterion for satisfying computer-generated hologram (CGH) patterns calculated on the basis of the iterative Fourier transform algorithm (IFTA) or Fresnel (near-field)/Fraunhofer (far-field) diffraction equations, derived from scalar diffraction theory. The linear material for modulation is the main assumption of the theory. However, the intrinsic electro-optical response of liquid crystal is nonlinear. The measured intensity curve can be converted to phase based on following equation:

$$\frac{I}{I_0} = \cos^2 \chi - \sin 2\phi \sin 2(\phi - \chi) \sin^2\left(\frac{\delta}{2}\right) \quad (1)$$

where I/I_0 is the normalized intensity, χ is the angle between the polarizer and the analyzer, ϕ is the angle between the LC director and the polarizer, and δ is the phase retardation. A nonlinear phase curve containing the sine or cosine function is tuned to a linear phase response using the look-up table (LUT) method [43]. The CGH simulation cannot correctly project its result without linear correction of LCoS-SLM. To solve this issue, "Linear Phase Calibration" is the first step for LCoS-SLM. The ease of calibration is closely related to the driving scheme in the control circuit. The digital driving scheme uses pulse width modulation (PWM) to generate the gray scale that can be divided into finer 0- and 1-bit planes. This makes programming of linear calibration much easier than the backplane using

analog driving scheme. The average phase accuracy error (APAE%) and the root mean square (RMS) methods can be applied to evaluate the phase linearity of all gray levels:

$$APAE\% : \frac{\sum_{GL=0}^{GL=255} (|\delta_m(LUT(GL)) - \delta_i(GL)|)}{256} \tag{2}$$

$$RMS : \sqrt{\frac{\sum_{GL=0}^{GL=255} (\delta_m(LUT(GL)) - \delta_i(GL))^2}{256}} \tag{3}$$

where GL is gray level, LUT is look-up table, and δ is the measured phase value. Let us take our PCM-2-01-633 LCoS-SLM panels with 1920×1080 resolution and 60–144 Hz picture frame rate as an example. A design with high-programmed pulse switching at $\Delta V = 5$ V of the IC backplane was applied to reach a liquid crystal response within 4 ms. The phase linearity can be adjusted down to 1.08% (APAE%) and 0.024π radians (RMS). In general, achieving ideal phase linearity in an analog driving LCoS-SLM is more difficult than in a digital driving scheme without additional optical compensation [44]. The Meadowlark Optics LCoS-SLM can achieve high-speed response (2–5 ms) while maintaining a certain degree of linearity in a low-resolution panel (256×256) using analog driving [45], but not in their higher resolution (HSP512 and HSP1920) LCoS panels. The LCoS panel with an analog drive from Hamamatsu Photonics can achieve ultra-high linearity (0.03π radians (RMS)) at a lower resolution (792×600) and 60 Hz picture frame rate. However, to maintain ultra-high phase linearity, a thicker LC cell gap and lower operation voltage are required. The drawback of this setup is a slower response time (>30 ms).

5. Phase Precision

Besides the linear phase response in all gray scales, another issue is the phase precision over the entire active area. Phase precision has been discussed in many different forms, such as “spatially varying phase response” (SVPR) [46], “spatially resolved phase response” (SRPR) [47], “multipoint phase modulation” [48–51], “uniformity metrology” [52–55], “wavefront distortion” [56], “optical flatness”, etc. The phase precision quality of an LCoS-SLM panel can be defined by using the above-mentioned evaluation methods. The current four major LCoS SLM manufacturers provide the following phase precision standards: (1) the latest 4K2K GAEA-2 from Holoeye Photonics—the optical flatness can be adjusted from $\lambda/6.6$ to $\lambda/10$ (RMS) after phase compensation; (2) the high phase precision LCoS panel from Hamamatsu Photonics—the phase precision has to be $\lambda/50$ mean standard deviations (mSTD) after phase compensation; (3) the latest HSP1920 from Meadowlark Optics—the phase precision can be adjusted from $\lambda/7$ to $\lambda/20$ (RMS); and (4) the latest SLM-200 from Santeq Corp.—its wavefront distortion is greater than λ , but can be reduced to $\lambda/40$ after compensation. We adopt the mSTD from 256 (8 bits) gray scales to evaluate the phase precision before compensation, defined as:

$$mSTD(x, y) = \frac{\sum_{GL=0}^{GL=255} \sqrt{\frac{\sum_{mxy=1}^{mxy=\max} (\delta_{mxy}(LUT_{global}(GL), x, y) - \overline{\delta_{mxy}(LUT_{global}(GL), x, y)})^2}{Total\ mxy}}}{256} \tag{4}$$

where δ is the phase measurement based on the LUT at different gray levels, and x and y refer to the specific area measured over the whole panel as shown in Figure 5. Fewer or more sections of mxy is dependent on the beam size or characterization method.

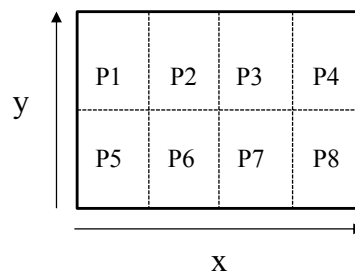


Figure 5. The scheme of eight different sessions (mxy) over the entire active area in the Equation (4).

The following formula represents various factors that affect phase modulation:

$$\delta_{\text{total}}(V, T, x, y) = \Delta\delta(T, V, x, y) + \delta_o(x, y) \quad (5)$$

Factors that can affect the output wavefront (δ_{total}) of an SLM can be attributed to modulated terms ($\Delta\delta$) and non-modulated terms (δ_o). The modulated terms ($\Delta\delta$) can be rewritten as follows to specify the effects on LC birefringence (Δn) and cell gap (d):

$$\delta_{\text{total}}(V, T, x, y) = \Delta n(T(x, y), V(x, y))d(x, y) + \delta_o(x, y) \quad (6)$$

In the non-modulated term, unevenness may arise due to the surface flatness of LCoS IC-backplane after the chemical mechanical polishing (CMP) process [57], the surface flatness of LC alignment layer, or other components like Indium Tin Oxide (ITO) cover glass. The main source of imprecision of the SLM phase is the modulation term. The three factors can be recognized across the entire LCoS panel (x, y) as following: (1) the cell gap uniformity (d); (2) the temperature effect (T) of the SLM panel, and (3) the voltage uniformity (V) from pixels. The uniformity of the LC layer is the most important parameter for the output wavefront. It requires a high assembly precision to control the cell gap uniformity error of $\sim 1\%$ over the entire active area in the liquid crystal assembly (LCA) process (assuming no internal damage to the IC backplane during LCA). This becomes more difficult if the cell gap is below $1.6 \mu\text{m}$. The problem of rising temperature may be caused by either a high-power light source or the thermal effect from IC backplane. The output wavefront unevenness could result from non-uniform backplane temperature due to poor heat dissipation design in the optical system. This effect can be minimized if the operating temperature of the LCoS-SLM backplane can be evenly controlled at a constant temperature. Most of the problems that affect the phase precision of the panel are mainly due to the non-uniform LC cell gap and the uneven output voltage at each pixel. The non-uniform voltage occurs mainly on the analog drive's LCoS panel because the analog drive uses a dynamic random access memory (DRAM) capacitive method to supply different voltages for different gray levels. By contrast, different gray levels in the LCoS panel with digital drive are generated by the static random access memory (SRAM) to determine the voltage difference ($\Delta V = V_w - V_b$, in which V_w is V_{white} , and V_b is V_{black}) in conjunction with different time sequences (namely, pulse width) driving design. As a result, the digital driving scheme has the DC balance characteristic, which has fewer problems with voltage non-uniformity. In addition, the data addressing speed of the digital drive chip is very fast ($\text{ns} \sim \mu\text{s}$). This will not be the main cause of the unevenness and inaccuracy of the spatial phase, even if the digital driver adopts the time sequential driving scheme. The major problem associated with digital time sequential driving design is "phase flicker", which will be discussed in more detail in Section 6, which is devoted to phase stability.

The default setting may be off or not satisfied for the design applications after receiving the SLM panel from the vendors. As a result, phase linearity calibration and phase precision compensation have been hot topics for SLM research. Various reported phase compensation methods can be roughly divided into two categories: one is by electronic compensation method, and the other is by optical compensation method. Most of the LCoS-SLM driver board settings are not open to users. As a result,

the electronic compensation method may only be available to original manufacturers. Most SLM users applied optical algorithms to achieve high-quality wavefronts by correcting wavefront aberration using the Zernike polynomials algorithm. The algorithm can correct the most serious wavefront distortions, but is limited to a circular aperture due to the principle of Zernike polynomials. The compensation is inappropriate for square or rectangular apertures, such as the LCoS-SLM panel. For example, P512 series products from Boulder Nonlinear Systems (BNS), have wavefront distortion value of 1.2λ before compensation. They can be optically compensated to $\lambda/4$ [58] but not up to the high phase precision standard ($<\lambda/50$). Their latest product, HSP1920, adopted electronic compensation for square sub-apertures in 2018. The wavefront distortion value can be greatly enhanced from $\lambda/7$ to $\lambda/20$ (RMS). Even though Zernike polynomials are not fully applicable to a square aperture to correct high-end aberrations, other research teams continue to improve or seek alternative optical algorithms for phase compensation. Xu et al. proposed to use the Zernike polynomials method plus least mean square fitting to adjust the LCoS uniformity of $1 \times 12,288$ pixels. Their result suggested that the RMS value of the inherent wave-front distortion can be suppressed approximately to $\lambda/34$ [59]. Engström et al. proposed using the principle of 7th-order 3D polynomials to compensate for the phase uniformity of the HSPDM512 product of BNS in the form of 64×64 pixels. It uses the peak-to-valley value as an evaluation method for wavefront distortion. It was shown that the maximum error amount can be corrected from $0.6\text{--}0.8 \pi$ to 0.3π [46]. This method effectively corrects the error, but increases the overall computation time for the CGH phase compensation. The computation time can be accelerated up to 0.13 ms per CGH by using the Compute Unified Device Architecture (CUDA) encoding program. This result may not affect real-time operation on its panel, but will significantly prolong the computation time when using higher resolution ($>4K2K$) SLM with a high frame rate (>240 Hz). Either internal compensation using a chip circuit, or external compensation using an FPGA or Application Specific Integrated Circuit (ASIC) driver board are the better solutions to avoid extra load of computing the compensation algorithm in CGHs. In addition, it is possible to divide the active area into several unit blocks. The phase correction look-up table of each unit block (LUT(x, y)) can be performed individually to achieve high-precision modulation of the wavefront.

To demonstrate the phase precision measurements, we compare three LCoS-SLM at $\lambda = 633$ nm, PCM-2-01-633, PCU-3-01-633 (prepared in house), and LETO (Holoeye), under the same digital driving scheme at a controlled temperature $T = 35$ °C. The phase linearity errors (APAE%) under the global LUT of the PCM-2-01 and PCU-3-01 are 1.08% and 1.24%, respectively. The phase linearity error (APAE%) of the LETO panel can be tuned from 2.20% to 0.71% by using our driving calibration instead of the default setting for production. The spatially varying phase responses (SVPR) of the three different LCoS-SLM panels are measured after the phase linearity error of the global phase response is adjusted to around 1.0%. On each LCoS-SLM panel, a phase shift is estimated from eight different sections (P-1 to P-8 as shown in Figure 5) covering the entire panel's active area, as shown in Figure 6. The mSTD of phase precision error from PCM-2-01-633 and LETO are 0.035π radians ($\lambda/57$) and 0.043π radians ($\lambda/47$), respectively. For the PCU-3-01-633 panel, it is 0.067π ($\lambda/30$). The cell gap uniformity errors of PCM-2-01-633 and PCU-3-01-633 panels are 0.68% and 0.99%, respectively, which are evaluated at 12 points using white light spectroscopy (Lambda 950 from Perkin Elmer). This rules out the panel thickness variation issue of these two panels. The problem of uniformity in data addressing is further evaluated by DAC (digital-to-analog converter) and BGP (background pixel) data driving modes. The measurement of DAC mode bypasses dynamic data addressing in pixel memory. It only fixed the IC backplane electrode voltage (V_0, V_1) and adjusted the voltage different of V_{ITO} ($V_{ITO-H} - V_{ITO-L}$). The DAC mode, in short, simply provides the V_{rms} of full panel as one large single pixel. The BGP mode is implanted in the linear global LUT data addressing in the SRAM. The phase precision errors from PCM-2-01-633 and PCU-3-01-633 panels are 0.035 and 0.039π radians under DAC mode, but 0.035 and 0.067π radians with BGP mode, respectively. It is clear that the non-uniform data addressing in pixel level is the cause of larger SVPR value in the PCU-3-01-633 LCoS-SLM panel.

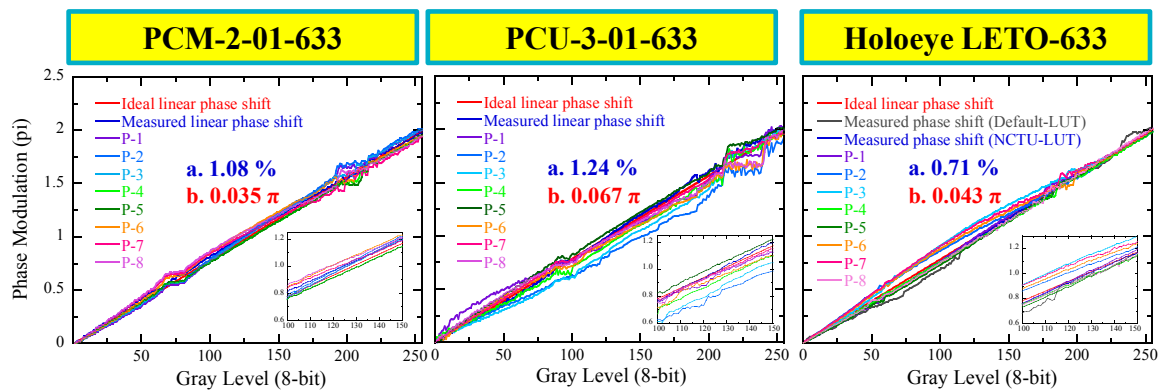


Figure 6. The phase precision measurements of 3 different LCoS-SLMs. The inserts are the enlarged figure from gray level 100 to 150. The values in a. is the phase linearity error (APAE%); and b. is the mSTD of phase precision error.

6. Phase Stability

Phase stability in terms of time-fluctuation is the major weakness in PWM devices [60–62]. The different working principles of digital and analog driving schemes are shown in Figure 7 [63].

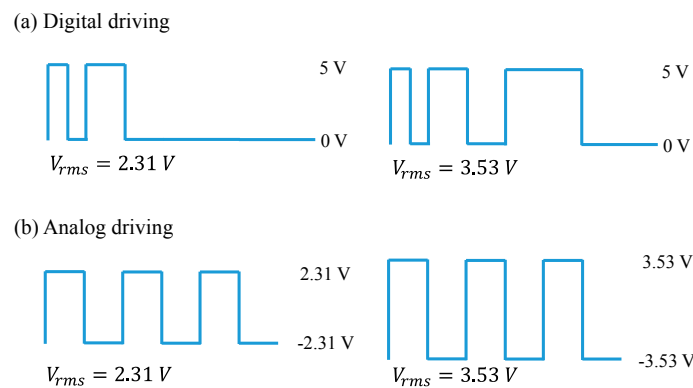


Figure 7. The schemes of digital and analog driving waveforms.

V_{rms} can be calculated between the voltages (v_1 and v_2) applied in a short period of time (t_1 and t_2), as shown in the following equation:

$$V_{rms} = \sqrt{\frac{t_1 v_1^2 + t_2 v_2^2}{t_1 + t_2}} \tag{7}$$

Briefly, the digital driving scheme maintains a constant voltage (ΔV) while changing the pulse width to achieve the various V_{rms} values. The flicker yields from charging and discharging phenomenon within the sub-millisecond because the instantaneous voltage recognized by liquid crystal is the time ratio between the pulses at constant voltage (ΔV) in the driving cycle. On the other hand, the analog driving scheme achieves its V_{rms} values by changing the applied voltages at constant frequency. There is no obvious flicker in the analog driving scheme because the instantaneous voltage resembling V_{rms} is perceived by the liquid crystals without a noticeable charging and discharging phenomenon. Figure 8 shows the phase flicker generated from digital drive at gray level 70 using a LETO panel.

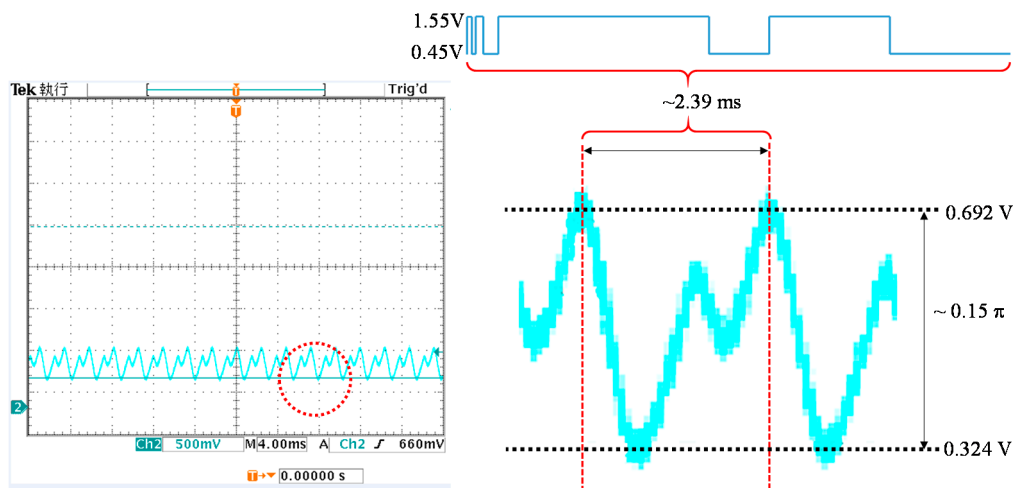


Figure 8. The digital drive waveform and its corresponding phase flicker measured in the high-resolution oscilloscope.

Two measurements with different photodetector settings are performed to show the phase shift and phase flicker in each gray scale. The digital color light sensor (TCS-3404 from TAOS) is adopted as a photodetector for the phase linearity and phase precision measurements. The data are taken every 100 ms for each gray level, which is presented as a red line for measured phase shift in Figures 9 and 10. Temporal fluctuation is measured by a polarization interferometer (PIF) with a polarizer and an analyzer aligned at 45° with respect to the rubbing direction. The photodetector is connected to a high-resolution oscilloscope (300 MHz 2.5 GS/s from Tektronix TDS3034B). The data are taken every few μs for each gray level. The phase flicker is determined by the maximum and minimum phase values of each gray level. The ΔV values of these three panels are 5.00V, 2.12V and 1.10V for PCM-2-01-633, PCU-3-01-633, and LETO, respectively. Their LC response times are 3.8, 2.8, and 20.7 ms, and their resulting mean standard deviations (mSTD) are 0.14π , 0.12π , and 0.04π , respectively. The small ΔV setting in the digital driving scheme is able to minimize the flicker, as suggested in the data. To validate the assumption, the thicker cell gap of PCU-3-01-633 LCoS-SLM filled with the same LC was prepared to achieve 2π radius phase shift at lower $\Delta V = 1.04$ V. The mSTD of the thicker LCoS-SLM (named PCU-3-01-633-2) is only 0.04π . The response time, regrettably, slows considerably from 2.8 to 9.6 ms. To summarize the phase stability in the digital drive LCoS-SLM, it is able to minimize the flicker when smaller ΔV is applied for 2π radius phase shift modulation. The slower LC response, however, is inevitable with this approach.

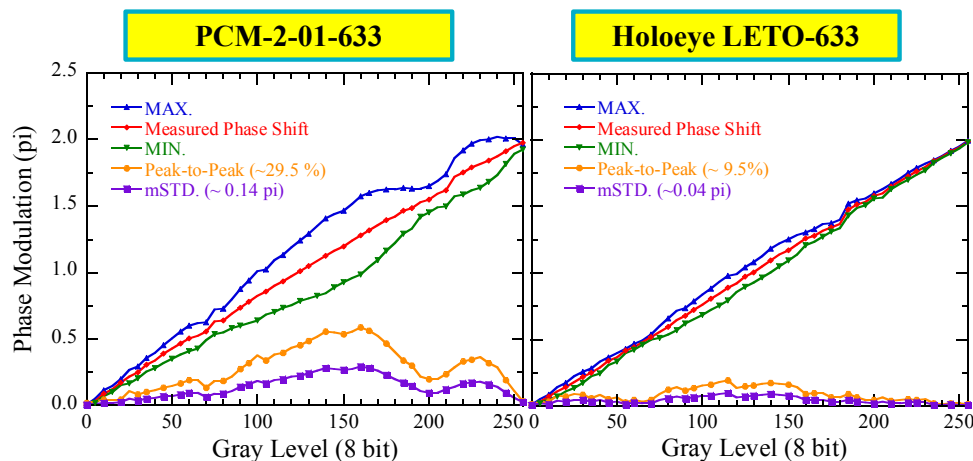


Figure 9. The phase stability measurements of PCM-2-01-633, and LETO LCoS-SLMs at 633 nm.

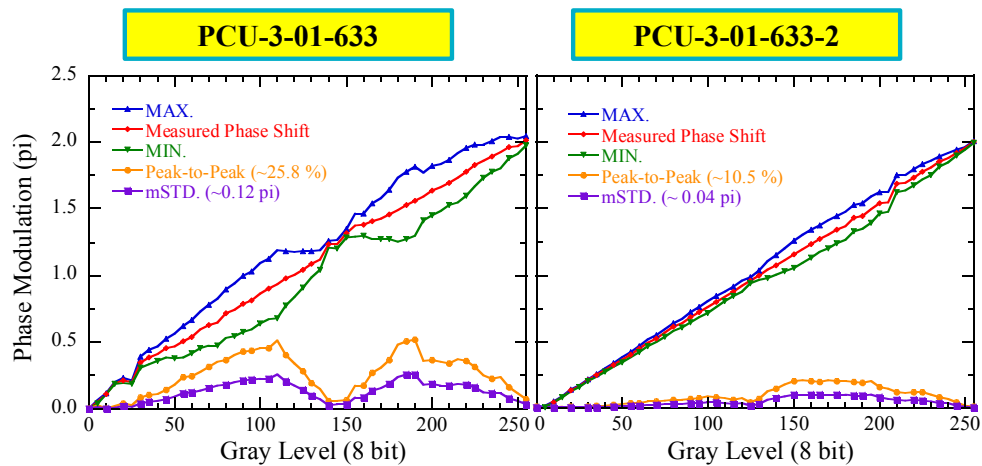


Figure 10. The phase stability measurements of PCU-3-01-633 with two different cell gap LCoS-SLMs at 633 nm.

7. Phase Accuracy

Phase accuracy is determined by the projected image. The pixel-level spatially anamorphic phenomenon provides a way to appraise the phase accuracy from a linear phase calibrated panel [64–67]. The phase error is quantified by employing various spatial frequencies and grating directions deviating from an ideal crenel-like binary grating. The deviation from the ideal binary grating is related to the non-desired LC fringing field and reverse-tilt effects [67]. To measure phase modulation, a 0th gray level (GL-0) is defined as 100% of the intensity of the light reflected from the panel. The ± 1 st-order light intensity is determined by the spatial frequency and orientation of the binary grating. The binary-grating differences are created by changing the corresponding gray-level pairs, in which the gray level of the reference pixel is fixed as GL-0 and changed 5–255 gray levels (GL-5–255) in the counterpart. The PCM-2-01-633, PCU-3-01-633, and LETO LCoS-SLM panels, possessing the same backplane design, are compared regarding their pixel-level spatially anamorphic phenomenon at $\lambda = 633$ nm. In Figure 11, H and V represented horizontal and vertical grating, and P is the binary grating pairs at different periods: 1 + 1, 2 + 2, and 6 + 6 pixels/grating, which correspond to spatial frequencies of 76.8, 38.4, and 12.8 lines/mm, respectively. The average error of the pixel-level phase accuracy of the horizontal and vertical grating is 9% for PCM-2-01-633, 7.7% for PCU-3-01-633, and 12% for LETO. This result suggests that PCU-3-01-633 with a small cell gap can effectively suppress the pixel-level phase accuracy error. However, the 1 + 1 in vertical grating still has 10% error. Small average phase-accuracy errors in both horizontal and vertical grating directions are necessary at the same time to minimize the error for the application of 2D CGH patterns.

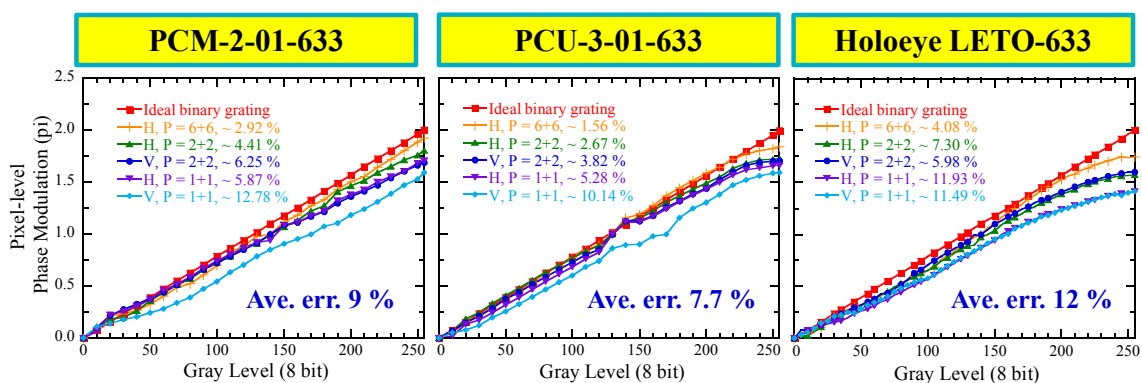


Figure 11. The phase accuracy measurements of three different LCoS-SLMs at 633 nm.

8. Conclusions

LCoS technology has been developed for nearly four decades. However, due to its higher production cost and lower performance than DMD, the latter continues to dominating the present-day projection display market. Recently, high-resolution and fine pixel size LCoS backplanes have been produced by several vendors, and appear to offer more promising spatial light modulation than full high definition displays. Techniques for assessing various characteristics of high-resolution LCoS SLMs have been discussed in this review. The physics and modeling of LCs in parallel aligned LCoS were not discussed in detail due to the limited length and scope of this paper. The physics and electro-optical effects of tilted LCs, and addressed digital driving in the parallel aligned LCoS, have been thoroughly discussed by Francés et al. and Martínez, et al. [68–70]. During the course of developing high quality SLM panels, the phase precision, phase stability, and output phase accuracy need to be meticulously adjusted on a linearly calibrated phase panel. The novel high-quality and well-calibrated panels can generate phase-only holography and project faithful CGH design, which can be useful for 2D/3D holographic video projectors and optical telecommunications. The high phase precision, fast response time, good phase stability, and high phase accuracy of LCoS panels opens more possibilities for various applications, as discussed above. Though the benefits of LCoS panels are many, endeavors should be made to offer them at a competitive price to the public in the near future.

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