

Course Syllabus

OSE 4952 Senior Design 2

Summer 2018

Friday 9:00 AM -11:50 AM BA1 0116

(Second section, 12:00 PM- 2:50 PM – attend with your group!)

Discussion Session: Wednesdays 10:00 AM – 10:50 AM

Instructor: Dr. David Hagan

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Office CREOL 209

407-823-6817

Office Hours: By appointment, or stop by.

Catalog description: Development of the technical, communication, and team skills for successful design of optical and photonic systems. Preparation of project proposals for Senior Design II.

Prerequisites: OSE 4951 and OSE 4520; CR: OSE 4410 and OSE 4470 and C.I

Co-teaching with EEL 4915L Senior Design 2:

Photonic Science and Engineering students are expected to engage in interdisciplinary projects with electrical and computer engineering students. For this reason, students will attend all classes with the electrical and computer engineering students enrolled in EEL 4914L Senior Design 2. Most assignments will be common to both courses, however, Dr. Hagan will be responsible for assessment and grading of students enrolled in OSE 4952. The grading standards will be common to both courses.

In addition to attending all EEL 4915L classes, students must meet regularly with Dr. Hagan. The schedule for these meetings will be set up in the first week of the semester.

The instructor for EEL 4914C is:

Dr. Samuel Richie, HEC--444

Texts:

1. DESIGN FOR ELECTRICAL AND COMPUTER ENGINEERS, McGraw-Hill (chapter 3)
2. SENIOR DESIGN FOR ELECTRICAL AND COMPUTER ENGINEERINGS STUDENTS, Pearson Custom Publishing (**3 chapters**)

Reference materials: Depends upon the group's project. Consultation with appropriate faculty and professionals is encouraged. Cite all your sources for information in your reports.

Software: Varies by Project, Circuit Simulation Software, Schematic Capture Software, PCB Software, Matlab, Zeemax, Light Tools, etc.

Attendance in class is required. Assignments are due when collected by the instructor. All or only a part of the collected homework may be graded. All exams are mandatory. The final grade will be based on

your performance on attendance, exam performance, presentation performance, and final project documentation, as described later. In addition, failure to comply with course requirements or expectations may result in a lower grade as determined to be appropriate by the instructor.

Any act of academic dishonesty or unprofessional behavior will result in a failing grade on an exam or in the course.

Discussion Session: Wednesday 10:00 AM – 10:50 AM

This is an opportunity for student to discuss in a group with the Photonics Instructor, to ensure that progress is being made on schedule and to seek help with any technical problems. The time for this can be agreed upon between the students (as a group) and instructor. ECE group member do not have to attend these meetings.

Preliminary Course Information

The OSE 4951 and OSE 4952 Senior Design courses are intended to serve as capstone courses for the Photonic Science and Engineering Bachelor of Science Degree. **These courses subject the students to an environment unlike the majority of their previous curriculum.** Students will encounter aspects of engineering design not found in prior coursework. Students will be responsible for their own learning as a team. In other classes, students are given homework, quizzes, labs and tests in a structured and scheduled manner, but in Senior Design it is the team's responsibility to schedule their project, assign responsibilities, build the functioning device or system that meets specifications, document the results of the team's efforts in written reports.

Summary of primary activities in the semester:

Week (Approximate)	Topic
1	Introduction to semester activities.
2	Lecture on preparation of critical design review
3,4,5	Critical design review presentations: Groups in turn present a review of their project status. All students must attend and will grade these presentations. At this point, all details of design, specifications and constraints should be final.
6,7	No class – work on projects. Design Testing continues. Weekly design meetings, collecting data, recording in journal.
8,9	Mid-term demonstrations. – By now projects should be largely functional
8	End of semester (EOS) preparation lecture.
9	Final class

10	8-page conference paper due.
11	Final presentations
12	Final exam week. Final report due (120 pages)

Course goals: To provide students a complete design experience, including the necessity to set design goals and objectives, integrate knowledge, exercise engineering judgement, plan to meet a budget and a schedule, to work as a team member, and to communicate in writing.

Specific Course objectives:

This class is a required course for Photonic Science and Engineering students and serves as the first part of the capstone design course sequence. The course objectives are to enable students to:

- Learn standards based design practices
- Incorporate appropriate human factors into designs
- Develop knowledge of Engineering Economics
- Recognize and address ethical issues related to design and engineering
- Develop an understanding of the Engineering Design Process, Engineering Teamwork and Project Documentation

Learning Outcomes

Upon completing this course, the students will be able to:

- Identify specific goals of the designed system, including specifications and realistic constraints such as economic, environmental, social, political, ethical, health and safety, manufacturability, and sustainability constraints.
- Collect information on available components and standards related to design needs,
- Develop appropriate models and using computer tools for system analysis,
- Perform testing and failure analysis
- Prepare written proposals and delivering technical information through oral presentations, reports and logbooks
- Work in a team environment
- Recognize and address ethical issues related to design and engineering
- Develop a customer relationship and mentality

Assessment:

The final grade will be primarily based on the final project documentation and presentation. A rubric for how the final grade is determined is appended to the end of this syllabus. The faculty committee grading of the presentation will be a primary factor in determining the group grade. However, the overall course grade for an individual may be modified by timeliness of submission of assignments, attendance and by the individual's performance as part of the team. No grades are assigned, **on other elements, such as critical design review report, etc.** only indications of completion are recorded. If a student fails to demonstrate competency on an assignment, the assignment must be repeating until mastered. In cases where group members do not adequately contribute to the project, members may be dropped from the group and those students will receive a grade of F for the course.

Plus/minus grading is not used in Senior design.

To complete the course with a passing grade, the device or system must work as specified, meeting most specifications, at the demonstration which follows the final presentation. Failure to meet this requirement will result in a grade of F or I depending upon the circumstances as dictated by the course instructor. The final device or system must be a well-engineered system that is robust and tolerant to the environment. Optical components mounted on a breadboard are not acceptable. The machine shop course that students must take will help them learn how to make a robust mechanical system to house optical components, etc.

Final reports for **photonics** projects MUST contain a section at the end which contains results of testing of the final device or system. Without this testing, a letter grade will be deducted from photonics students. Where appropriate, this section should describe any explanation of why specifications or constraints were not met.

Computer Usage There will be no specific computer assignments. However, word processing is required for all documentation. Most projects will utilize computer simulation during the design, while other projects may be based on embedded control, the use of a personal computer, or single board computer applications where software and/or hardware development will be required.

Machine shop course Photonics senior design students must complete a short machine shop course offered by the college of Optics and Photonics before the end of senior design 1 semester.

Safety University policy requires that for safety reasons, at least **two people must be present in the laboratory premises** at any time. Violators will be asked to leave the laboratory premises. Since it is not possible to police this policy at all times, violators will be working entirely at their own risk.

Exams There will be no exams.

Expenses Neither the College of Optics and Photonics, nor the Department of Electrical Engineering and Computer Science will not provide project parts beyond what is available in school laboratories. The cost of the project may be exclusively yours, exclusively your sponsor's, or may be shared. The most common case is that the project is funded by the student group or by a sponsoring group, agency, or corporation.

NOTE: If project expenses are paid in part or in whole by the college or department then the project becomes the property of the school and it must remain at UCF.

Final Documentation The required final documentation consists of a formal technical document consisting of research, design, theory of operation, construction and **testing**.

Laboratory No formal laboratory work is required. However, virtually all projects require hardware prototyping which will include construction and testing. Laboratory space and facilities will be available for this purpose.

Permission to use the Photonics senior design lab will be obtained through Mr. Michael McKee. In order to protect project installations, only students that are registered in the class will be allowed in the lab. He will have you take some short safety coursework prior to being given keys. You can also work in the EECS senior design laboratory during non-business hours and on weekends by using college keycards, and if needed requesting entry to the engineering building from the UCF Police Department. Identification will be required. Due to the policy stated below, the police will not provide entry to a single student. A minimum of two students are required when working in the laboratory.

WARNING

University policy requires that for safety reasons, at least **two people must be present in the laboratory premises** at any time. Violators will be asked to leave the laboratory premises. Since it is not possible to police this policy at all times, violators will be working entirely at their own risk.

In-Class Presentations

During the course of the two semesters, each group will make at least two presentations to the rest of the class during normal lecture or laboratory hours. This will serve as a critical design review for your project. The schedule for these presentations will be set just prior to this time.

Consultation

Consulting on each project will be available either from the course instructor or from any other Optics or ECE Department faculty member who has expertise on the subject of your project. Each team is encouraged to find a faculty member who will act as a technical advisor for the project. Appointments should be made for consultation times.

Academic activity:

Students' academic activity is required by UCF to be recorded at the beginning of each course. Failure to do so may result in a delay in the disbursement of your financial aid. **The assignment to satisfy this requirement is for all students to submit their group number on or before 12:00 PM (noon) on May 18, 2018.**

University of Central Florida
College of Optics and Photonics
OSE 4951/4952

Outline of Senior Design I and II Grading Rubrics

The final goal is to design and build a workable prototype which can be demonstrated to faculty reviewers during the one-hour presentation and demonstration (PD) at the end of Senior Design 2. This document first outlines the grading rubric for Senior Design II, followed the Senior Design I rubric, which is aimed at ensuring students' success in Senior Design II. Due to the complex nature of senior design and the large number of students and projects, this document may not cover all eventualities. Instead, students should use it as a general guideline to understand the grading policy for Photonics Senior Design 1 and 2 at UCF.

Note that plus/minus grading is not used in senior design.

Senior Design II Grading Rubric:

Group Base Grade (GBG):

The based grade for the group in SD2 is determined by averaging over reviewer panel scores after PD with minor adjustment by the instructors (for example, accounting for reviewer bias, level of photonics content, etc.). Averaging score is between 90-100 (A), 80-90 (B), 70-80 (C), 60-70 (D), 60 and below (F). The Reviewer scorings form can be found in the file "Project Reviewer Evaluation Form" in webcourses.

Note 1: It is the group's responsibility to form a review panel and submit Project Reviewer Commitment Form on time. Panels for Photonics groups must have representation from Optics and Photonics Faculty. If the group fails to form a panel, then the group will receive F.

Note 2: If the group fails to show up at scheduled presentation or arrive late for 15 minutes or more, then the group will receive F. If one member fails to show up at the final presentation, then this individual will receive F and redo SD1 and SD2.

Note 3: If the group fails to demonstrate their project as workable during the 10-minute demonstration time allocated within the one hour PD to reviewer panel, then the group needs to reschedule the demo within a week (before the final submission date) and receive one letter grade deduction in GBG. If the group cannot demonstrate their project to be workable by then, then the group will receive a grade of F.

Note 4: The Photonics Science and Engineering program requires that the project must have substantial photonics design content, and the photonics systems must be well-constructed consistent with the intended use of the product (not using tape, flimsy materials and usually not optical breadboard). Also, the final resort must include results of testing of the final device or system. The ECE program requires that the project include substantial PCB design. Noncompliance may affect photonics and ECE student differently – see part (A) on individual grades below.

For any group that receives a grade of F, the whole group must EITHER: Redo SD2 if the whole group can re-enroll together OR: redo SD1 if the whole group cannot hold together or if the Photonics and ECE instructors decide that the group needs to be broken up.

Group Final Grade: Once the group base grade (GBG) is decided, the Photonics and ECE instructors will check the following items to determine the Group Final Grade. Failure to meet the any one of the following requirements will result one letter grade deduction from the GBG. Multiple failures may result in multiple letter grade deductions.

- (1) Does the SD2 final report meet the requirements? (page, content, submitting on time, hard copy, soft copy). The SD2 report requirements are identical to the SD1 report requirement.
- (2) Were all group activities on time? (CDR presentation absence? Final Presentation on time? CDR meeting on time, middle term demo on time? reviewer commitment form on time?)
- (3) Is the group Website ready? Has the group presented at Senior Design Showcase?

For most groups, all team members will typically receive the GFG as his or her individual grade, but some groups each member may receive a different grade according to the following rules.

Justification of each individual grade:

Each team member's grade can be altered from GFG, i.e., each team member may receive a different grade due to following factors. Multiple violations may result multiple letter grade deduction. For example, a student who fails to attend ABET section for 3 times may receive 3 letter grade deductions.

- (A) Issues with clearly identifiable parts of the project: For example if the photonics part of the project is trivial, then photonics student(s) will receive a lower grade, or if the PCB does not work, then the ECE student will receive a lower grade. Additionally, if the final resort does not include evidence of testing of the final device or system, the photonics student(s) will receive a letter grade penalty. Also, if, for example, faculty reviewers note that "power did not work", the person in charge of power may receive a lower grade. Another example, "optics part is great", so the person in charge of optics may receive a better grade).
- (B) Peer review form (team member peer review form can alter an individual grade, either lower or higher)
- (C) Instructor judgement based on whole SD2 performance and attendance of each individual at "ABET" classes, or attendance of group meetings with instructors.

Senior Design I Grading Rubric:

Group Base Grade (GBG):

The base grade for the group in SD1 is determined by three factors: (1) Is the project running on time? (2) Does the project contain substantial design? (3) Do the reports meet the requirement?

- (1) Is the project running on time?** At the end of SD1, each team must show evidence of component comparison and selection Also the group must have received all major components and have performed breadboard or development board testing of all critical components. This testing must be sufficient to assure the instructors that the project has a good chance of working by the end of senior design 2. In an A grade report, we need to see evidence of all of the above.

- (2) Does the project contain substantial design?** If the project does not contain substantial design, (particularly optical/photonics design for PSE students and PCB design for ECE students), then the whole project will run into problems in SD2. ECE students must go beyond amateurish or hobbyist-like activities. For example, today, a hobbyist can spend several afternoons to order a few development boards and download a few software programs to perform some nice functions or demonstrate some nice actions. But these hobbyists typically have no idea what are behind these boards and software. Similar, you can buy photodiodes, LEDs, lasers, etc. that come with spec sheets that offer up circuit designs. You, as a photonics engineer, must add something more than this. Your project must, to some extent, rely on the advanced concepts you have learned in your photonics coursework. If the photonics part of the project is something an electrical engineer could have done, then it is not going to merit an A-grade. Also, your ECE team mates must comply with the ECE Major PCB policy, that is, each team must have substantial PCB design and implementation in their project. To ensure this, at the end of SD1, we must see in the A grade report substantial schematic design which can be turned into PCB layout in SD2.
- (3) Does the report meet the final report requirement?** Detail can be found in Project Documentation Guidelines. In summary, length: 30 originally authored pages per person; line space: 1; page size: 8.5" x 11", with 1" margins (top, right, and bottom), 1.5" left for binding; paragraph: fully justified. Starting from Executive summary, containing Standard and design constraints. Content that is superfluous, irrelevant, or does not directly relate to your project will not be counted towards the page count. In summary, put limitation on the following: white spaces, copy of data sheet material and tutorial material, photos of common items, debug windows, software codes, etc.

Group Final Grade (GFG): Once we decide (GBG), the instructor will check following items to determine GFG. Failure to meet the following requirement (any one aspect) will result one letter grade deduction from GBG. Multiple failures may result multiple letter grade deduction.

- (1) Was the SD1 final report submitted on time? (Hard copy, softcopy).
- (2) Were all group activities on time? (Divide and Conquer submission and team meeting on time? 60 page, 100-page Draft document submission on time, sufficient page, and meeting on time?)

The Instructors will further check the following factors to determine individual grades:

- (A) Attendance at "ABET," classes, bootcamp, and Team meetings with instructors (late or absence). Each absence may result one letter grade deduction and can be accumulated.
- (B) Passing quizzes. Failure to pass one quiz may result one letter grade deduction, which can be accumulated.
- (C) Instructor judgement based on whole SD1 performance of each individual or group peer review form. Instructor may request each individual member in a team to submit his or her individual portion of contribution to the team work or using Group Peer Review Form. These mechanisms aim to identify those individuals who substantially fail to deliver their parts of work in SD1. As a consequence, some individuals may be removed from the team and have to redo SD1 in future.